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|-----------------------------------|--|-------------------------|---|---------------------------------------|
| <b>Notice of References Cited</b> |  | Application/Control No. | Applicant(s)/Patent Under Reexamination<br>09/895,524 MENA ET AL. |                                       |
|                                   |  | Examiner                | Art Unit  | Page 1 of 1<br>George Fourson<br>2823 |

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name                 | Classification |
|---|---|--|-----------------|----------------------|----------------|
|   | A | US-6,046,106                                     | 04-2000         | Tran et al.          | 438/660        |
|   | B | US-6,197,691                                     | 03-2001         | Lee, James Yong Meng | 438/691        |
|   | C | US-  |                 |                      |                |
|   | D | US-  |                 |                      |                |
|   | E | US-  |                 |                      |                |
|   | F | US-  |                 |                      |                |
|   | G | US-  |                 |                      |                |
|   | H | US-  |                 |                      |                |
|   | I | US-  |                 |                      |                |
|   | J | US-  |                 |                      |                |
|   | K | US-  |                 |                      |                |
|   | L | US-  |                 |                      |                |
|   | M | US-  |                 |                      |                |

**FOREIGN PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
|   | N |  |                 |         |      |                |
|   | O |  |                 |         |      |                |
|   | P |  |                 |         |      |                |
|   | Q |  |                 |         |      |                |
|   | R |  |                 |         |      |                |
|   | S |  |                 |         |      |                |
|   | T |  |                 |         |      |                |

**NON-PATENT DOCUMENTS**

|   |   |  |
|---|---|--|
| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)                                  |
|   | U | Wolf, S., "Silicon Processing for the VLSI Era - Vol.2, Process Integration", Lattice Press, 1990, pp.194-197 and 232-235. |
|   | V |  |
|   | W |  |
|   | X |  |

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